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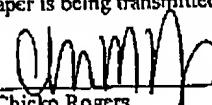
PATENT

S/N 10/754,416

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	KAWAKITA, ET AL.	Examiner:	THIEM D. PHAN
Serial No.:	10/754,416	Group Art Unit:	3729
Filed:	JANUARY 9, 2004	Docket No.:	10873.0787USD1
Title:	CIRCUIT BOARD ELECTRICALLY INSULATING MATERIAL, CIRCUIT BOARD AND METHOD FOR MANUFACTURING THE SAME		

CERTIFICATE UNDER 37 CFR 1.6(d): I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on March 7, 2006.

By:   
Name: Chicko Rogers

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CHANGE OF ADDRESS

Dear Sir:

Please change the correspondence address for the above-referenced patent application to:

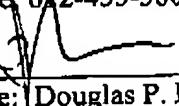
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If there are any questions regarding this matter, please call the undersigned at  
612.455.3800.

Respectfully submitted,

53148  
PATENT TRADEMARK OFFICE

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Date: March 7, 2006